

Materials Design and Characterization Laboratory (MDCL)

The MDCL was established as the third research facility of the Institute for Solid State Physics (ISSP) when the latter was reorganized in May 1996. Its aim is to promote material science with an emphasis on the “DSC cycle”, where DSC stands for design, synthesis and characterization, three processes for developing new materials.

The MDCL consists of two sections, Materials Design (MD) section and Materials Synthesis and Characterization (MSC) section. The Supercomputer Center of the ISSP (SCC-ISSP) is placed in the MD section, while in the MSC section there are six laboratories for joint use; Materials Synthesis Laboratory, Chemical Analysis Laboratory, X-ray Diffraction Laboratory, Electron Microscope Laboratory, Electromagnetic Measurement Laboratory, and Spectroscopy Laboratory.

Almost all the facilities of the MDCL are open to scientists in Japan through the User Programs conducted by two steering committees of the MDCL. One is the steering committee of the SCC-ISSP, under which the Supercomputer Project Advisory Committee is placed for reviewing proposals. The other is the steering committee of the MSC facilities. More than half of the members of these committees are from the outside of ISSP.

PREFACE

The Supercomputer Center (SCC) is a part of the Materials Design and Characterization Laboratory (MDCL) of ISSP. Its mission is to serve the whole community of computational condensed-matter physics of Japan providing it with high performance computing environment. In particular, the SCC selectively promotes and supports large-scale computations. For this purpose, the SCC invites proposals for supercomputer-aided research projects and hosts the Steering Committee, as mentioned below, that evaluates the proposals.

The ISSP supercomputer system consists of two subsystems: System B, which is intended for more nodes with relatively loose connections. In July, 2015, the SCC replaced the two supercomputer subsystems (SGI Altix ICE 8400EX and NEC SX-9) to one new system (System B, SGI ICE XA/UV hybrid system). The system B consists of 1584 CPU nodes, 288 ACC nodes, and 19 FAT nodes. The CPU node has 2CPUs (Intel Xeon). The ACC node has 2CPUs (Intel Xeon) and 2GPUs (NVIDIA Tesla K40). The FAT node has 4CPUs (Intel Xeon) and large memory (1TB). The system B have totally 2.6 PFlops theoretical peak performance. System C - FUJITSU PRIMEHPC FX10 was installed in April, 2013. It is highly compatible with K computer, the largest supercomputer in Japan. System C consists of 384 nodes, and each node has 1 SPARC64TM IXfx CPU (16 cores) and 32 GB of memory. The system C have totally 90.8 TFlops.

The hardware administration is not the only function of the SCC. The ISSP started hosting Computational Materials Science Initiative (CMSI), a new activity of promoting materials science study with next-generation parallel supercomputing. This activity is financially supported by the MEXT HPCI strategic program, and in CMSI, a number of major Japanese research institutes in various branches of materials science are involved. The SCC supports the activities of CMSI as its major mission.

All staff members of university faculties or public research institutes in Japan are invited to propose research projects (called User Program). The proposals are evaluated by the Steering Committee of SCC. Pre-reviewing is done by the Supercomputer Project Advisory Committee. In school year 2015 totally 239 projects were approved.

The research projects are roughly classified into the following three (the number of projects approved):

- First-Principles Calculation of Materials Properties (123)
- Strongly Correlated Quantum Systems (31)
- Cooperative Phenomena in Complex, Macroscopic Systems (85)

All the three involve both methodology of computation and its applications. The results of the projects are reported in 'Activity Report 2015' of the SCC. Every year 3-4 projects are selected for "invited papers" and published at the beginning of the Activity Report. In the Activity Report 2015, the following three invited papers are included:

"Capacitance of Nanosized Capacitors Investigated using the Orbital-Separation Approach --Dead Layer Effect and Negative Capacitance",

Shusuke KASAMATSU, Satoshi WATANABE, Seungwu HAN, and Cheol Seong HWANG

"Correlation Effects in Topological Insulators",

Norio KAWAKAMI

"Multiscale Simulation Performed on ISSP Super Computer: Analysis of Entangled Polymer Melt Flow",

Takahiro MURASHIMA

June 3, 2016

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(Chairman of the steering committee, SCC, ISSP)

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